## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

TANABE, et al.

Application No.

**TBD** 

Filed:

February 10, 2004

For:

METHOD FOR FABRICATING SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

Expected

Group:

2829

Expected

L. Kilday

Examiner:

UNDER 37 CFR 1.97 AND 1.98

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 February 10, 2004

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents cited in connection with prior applications of the above-identified application, that is, in connection with at least one of Application No. 10/424,105, filed April 28, 2003; Application No. 09/939,600, filed August 28, 2001; Application No. 09/494,036, filed January 31, 2000; and Application No. 09/380,646, filed September 7, 1999.

Since each of No. 10/424,105, No. 09/939,600, No. 09/494,036 and No. 09/380,646 is being relied upon in the above-identified application under 35 USC 120, copies of the listed documents are not enclosed. See 37 CFR 1.98(d).

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified application. Accordingly, requirements of 37 CFR 1.97(b) are clearly satisfied.

Further pursuant to Applicants' duty of disclosure, the following comments are submitted in connection with technologically related subject matter:

- (1) The above-identified application and at least Serial Nos. 10/424,105, 09/494,036, 09/752,736, 09/752,766 and 09/752,737 claim priority under 35 USC 120 on application Serial No. 09/380,646, and are related to gate insulation film formation;
- (2) Application Serial No. 09/086,568 is directed to polymetal gate selective oxidation;
- (3) Application Serial No. 09/089,398, now U.S. Patent No. 6,066,508, and Continuing applications thereof (Serial No. 09/551,551, now U.S. Patent No. 6,319,860; Serial No. 09/982,173, now U.S. Patent No. 6,521,550; and Serial No. 10/013,729, now U.S. Patent No. 6,602,808) are directed to exhaust gas treatment;
- (4) Application Serial No. 09/314,956, now U.S. Patent No. 6,323,115 is directed to dual gate selective oxidation;
- (5) Application Serial No. 09/577,671, and a Continuation application thereof (Serial No. 09/635,270, now U.S. Patent No. 6,593,229) are directed to non-silicon oxide gate insulation;
- (6) Japanese Patent Application No. 09-050781, which has been laid-open as JP Laid-Open No. H09-172011 (June 30, 1997), is directed to insulation film formation; and
- (7) PCT/JP97/00188, published as WO97/28085 (August 7, 1997), is directed to water vapor generation.

501.37465CC8

In view of all of the foregoing, it is respectfully submitted that all applicable requirements of 37 CFR 1.97 and 1.98 have been satisfied, in connection with all documents on the enclosed List. Accordingly, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

To the extent necessary, Applicants petition for an extension of time under 37 CFR § 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Case No. 501.37465CC8) and please credit any excess fees to such Deposit Account.

Respectfully submitted,

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WIS/sjg

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO.	APPLICATION NO.		
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		APPLICANT			
INFORMAT	ION DISCLOSURE STATEMENT BY APPLICANT	TANABE, et al.			
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	AA						1
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AD	11-067747	3/9/1999	Japan	,		X		
AE	11-074264	3/16/1999	Japan			X		
AF	10-284484	10/23/1998	Japan			X		
AG	11-162971	6/18/1999	Japan			X		
AH	11-162970	6/18/1999	Japan			X		
Al	11-204517	7/30/1999	Japan			Х		
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		APPLICANT			
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Nakamura, et al., "Proceedings of the 45 <sup>th</sup> Symposium on Semiconductors and Integrated Circuits Technology", December 1-2, 1993. (full translation)
Tanabe, et al., PG Pub: US 2002/0004315 A1, 1/10/02
Tanabe, et al., PG Pub: US 2002/0019149 A1, 2/14/02
Tanabe, et al., PG Pub: US 2001/0009813 A1, 7/26/01
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		501.37465CC8	TBD
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(Use s	several sheets if necessary)	FILING DATE	EXPECTED GROUP
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	AO	Japanese Patent Office Action mailed May 16, 2000 for No	o. 3110456 with English translation
Examiner			Date Considered

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at <a href="www.uspto.gov">www.uspto.gov</a> or MPEP 901 04. 3 For Japanese patent documents, the

901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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